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APPLICANTS

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** CONTINUING DATA ***** *Yes TD*

This application is a CON of 08/803,145 02/19/1997 PAT 6,282,506

** FOREIGN APPLICATIONS ***** *Yes TD*

JAPAN 8-032152 02/20/1996

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature <i>M. M. M.</i> Initials <i>TD</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 26	TOTAL CLAIMS 14	INDEPENDENT CLAIMS 10
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TITLE

METHOD OF DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT UTILIZING A SCAN TEST FUNCTION

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